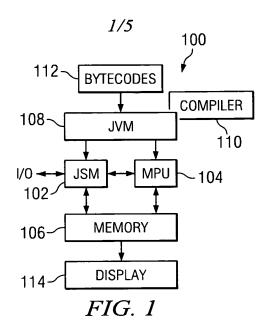
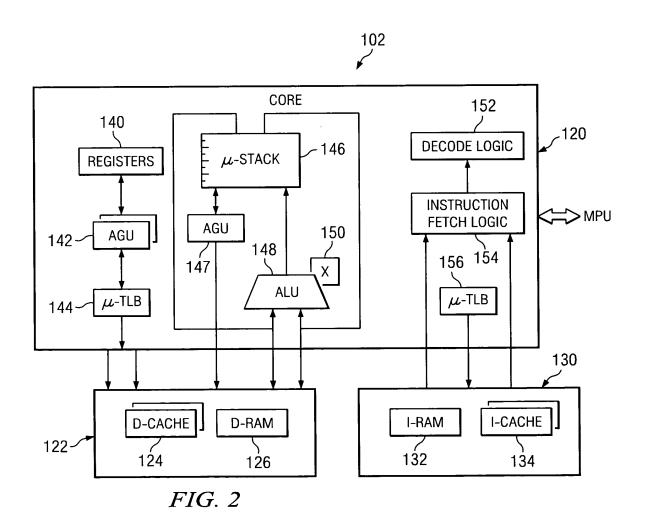
Attorney Docket No. TI-35433 (1962-05412) Applicants: Gerard CHAUVEL et al. Title: Mixed Stack-Based RISC Processor Express Mail Label No. EV303485200US





R0 GENERAL PURPOSE (GP) R1 GENERAL PURPOSE (GP) R2 GENERAL PURPOSE (GP) R3 GENERAL PURPOSE (GP) R4 PROGRAM COUNTER (PC) R5 GENERAL PURPOSE / LOCAL VARIABLE POINTER (LV) R6 STACK POINTER (SP) R7 TOP OF STACK (ToS)
R2 GENERAL PURPOSE (GP) R3 GENERAL PURPOSE (GP) R4 PROGRAM COUNTER (PC) R5 GENERAL PURPOSE / LOCAL VARIABLE POINTER (LV) R6 STACK POINTER (SP) R7 TOP OF STACK (ToS)
R3 GENERAL PURPOSE (GP) R4 PROGRAM COUNTER (PC) R5 GENERAL PURPOSE / LOCAL VARIABLE POINTER (LV) R6 STACK POINTER (SP) R7 TOP OF STACK (ToS)
R4 PROGRAM COUNTER (PC) R5 GENERAL PURPOSE / LOCAL VARIABLE POINTER (LV) R6 STACK POINTER (SP) R7 TOP OF STACK (ToS)
R5 GENERAL PURPOSE / LOCAL VARIABLE POINTER (LV) R6 STACK POINTER (SP) R7 TOP OF STACK (ToS)
R6 STACK POINTER (SP) R7 TOP OF STACK (ToS)
R7 TOP OF STACK (ToS)
R8 GENERAL PURPOSE / ADDRESS INDEX 0 (AI0)
R9 GENERAL PURPOSE / ADDRESS INDEX 1 (AI1)
R10 GENERAL PURPOSE (GP)
R11 GENERAL PURPOSE (GP)
R12 MICRO-PROGRAM COUNTER (MICRO-PC)
R13 GENERAL PURPOSE (GP)
R14 GENERAL PURPOSE / INDIRECT REGISTER INDEX (IRI)
R15 STATUS AND CONTROL (ST)

FIG. 3

